



* When the output voltage is 1.0kV or greater

Hipot (Withstanding Voltage) & Insulation Resistance Tester **TOS5300 Series**

The PWM amp system provides highly-stable output 5kV/100mA (500VA) AC Hipot (withstanding voltage) test 6kV/maximum output 50W DC Hipot (withstanding voltage) tester (TOS5301) 25V-1000V (7 steps), 500V or greater, up to 5.00GΩ Insulation Resistance test High-precision measurement ±1.5% of reading (with voltmeter 500V or higher,Ammeter 1mA or higher) Rise time/Fall time control Key lock function and Protection cover for key operation Equipped with USB interface



Highly stable

Newly developed, high-efficiency PWM switching amplifier !



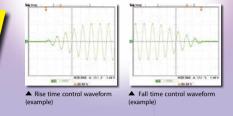
Providing a stable output of high voltage without being affected by AC line variation. Ensure the user to perform highly reliable testing with confidence, even in regions with large voltage variations. (Input voltage fluctuation rate: ±0.3%)

Output voltage wave form

control function of the applied voltage Equipped with a Rise time/

Fall time control function Prevents from an excessive stress applied to the EUT or for standard tests.

Rise/Fall Time



Pursuing usability and safety

All new design of the control panel and output terminals!

Eliminates the projected components of output terminals, and equips with a new type of the LOW terminal. Pursuing the improvement of safety and a convenience in production line, such as providing the protection cover for the front panel.

Output terminent : HIGH (red)

Right : LOW (black, with lock function)

Left : Hi



View with the protection cover removed from front panel

Supporting the World-wide input voltage

Reducing

the tact time

Increasing the productivity!

Capable of setting the test time from 0.1s

Usable globally !

Usable in any country without changing the input power supply.

Selectable output frequency !

The instrument not rely on the input power environment. Supplying the stable test voltage with 50/60 Hz frequencies.



Lightweight & **Compact design**

Increasing your work efficiency!

Weighs Approx. 15 kg 40% lighter than conventional models *Compared to TOS5300 and TOS8870A





When the output voltage is 1.0kV or greater

Hipot (Withstand-Voltage) & Insulation Resistance Tester



The "TOS5300 Series" is a series of test instruments used in Hipot (withstanding voltage) tests and insulation resistance tests, two of the four tests regarded as necessary for ensuring the safety of electrical products. With an output of 5 kV/100 mA (AC) and 6 kV/10 mA (DC), the series can be used in Hipot (withstanding voltage) & insulation resistance testing of electronic equipment and electronic parts, based on the requirements of IEC, EN, UL, VDE, JIS, and other international safety standards and the Electrical Appliance and Material Safety Law. Also, the test voltage stability is improved with the adoption of a newly developed switching amplifier. Since the output voltage can be kept constant even when the AC line voltage or frequency changes, consistent testing can be performed, even when the power supply environment is in an unstable region. The TOS5300 is also equipped with a number of features that are capable of meeting a variety of test needs. It is a new low-cost standard model that provides thorough operability, reliability and safety.

Applied test / Model	TOS5300	TOS5301	TOS5302
AC Hipot (Withstanding voltage) test (ACW)	~	~	~
DC Hipot (Withstanding voltage) test (DCW)		~	
Insulation Resistance test (IR)			~

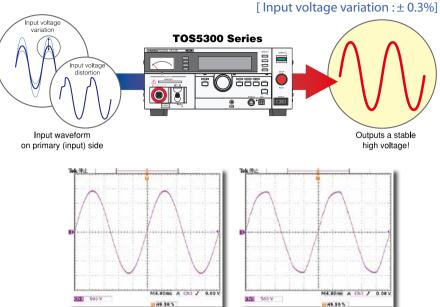
Features and Functions ● ACW: 5 kV/100 mA; DCW: 6 kV/50W ● IR: 25-1000V (7 steps)/500V or greater, up to 5.00 GΩ ● High-precision measurement ± 1.5% of reading (Voltage: 500V or greater ; Current: 1 mA or more) 🗨 Rise time / Fall time control function 🗣 Discharge function 🗣 World-wide input voltage 🗣 AUTO function 🗣 USB interface 🌑 Panel memory function (3 sets) • Key lock and Protection cover for panel operation

2

The achievement of AC Hipot (Withstanding voltage) testing with a constant stable output!

The output waveform is essential factor in Hipot (Withstanding voltage) testing!

A conventional Hipot (Withstanding voltage) tester boosts and outputs the AC line's input voltage through the use of a slide transformer. With this slide transformer system, input voltage fluctuations will affect the output, preventing tests from being performed properly. At times, the application of distortion voltage applied to the EUT may cause a failure of new product (accelerating a deterioration of components). Since the TOS5300 Series equips with a high-efficient PWM amplifier that can output a stable high-voltage without being affected by the variation of AC power line, users can perform "safe", "stable", and highly "reliable" tests with confidence, even in regions with large voltage variations.



▲ AC output waveform of the TOS5300 Series

AC output waveform of the slide transformer system

Realizing high-precision measurement with high-resolution and high-speed judgement

Equipped with a high-accuracy, high-resolution of True RMS measurement circuit, including a Voltmeter with $\pm 1.5\%$ of reading (500V or greater) / minimum resolution of 1V, and an Ammeter with $\pm 1.5\%$ of reading (1 mA or more) / minimum resolution of 1µA. In addition, it is also equipped with an Auto range function. The Lower limit judgment accuracy achieves a level of performance equivalent to the Upper limit judgment accuracy that enables to detect for such a poor contact or disconnections of test leads. Moreover, it realizes the fast judgment by the test time of 0.1 second, while reliable testing can be performed, thanks to high-precision, high-resolution, high-speed measurement and the judgment functions.



▲ AC Hipot (Withstanding voltage) test settings display (example)

Supporting the World-wide input voltage



Usable globally !

Usable in any country, without changing the input power supply.

Selectable output frequency !

The instrument not rely on the input power environment. Supplying the stable test voltage with 50/60 Hz frequencies.

Reducing the tact time

Reduction of the tact time leads to improve the productivity. However, it has been an issue that reducing the tact time may cause to worsen the measurement accuracy when the test time is faster than the measuring response speed. The TOS5300 series has been achieved to set the test time from 0.1s.

(Model TOS5301)

6kV/50WDC Hipot (Withstanding voltage) test

Capable to perform DC Hipot (Withstanding voltage) test up to 6 kV. (Model TOS5301) Equipped with a stable DC/DC converter with a low-ripple and the load variation of 3% or less.

Insulation resistance test for 25V to 1000V*

The TOS5302 is equipped with an insulation resistance tester. The test voltages can be set from 25V, 50V, 100V, 125V, 250V, 500V and 1000V. And for setting at 500V and above, it can perform the insulation resistance test up to 5.00 G Ω .

*At 500V and above, measurements up to 5.00 G Ω are possible.

Protection cover prevents physical operation error in the production site

Prevents a physical operation error by installing the protection cover on the control keys.



In many cases, workers on electronic equipment production lines and inspection lines are not technical experts. Therefore, it is possible that the operators may change setting conditions and make operation errors. In order to prevent from such cases, the TOS5300 is equipped with a key lock function and a protection cover to disable a physical key operation from the front panel.



Storing the protection cover for the key operation to the base of unit. ▶ During the periods of operation with the protection cover removed, such as when the test conditions are frequently changed and tests are performed repeatedly, the protection cover can be stored at the base.

New design of output terminal improves safety and functionality

Eliminates the projected components from the front panel. The new design of LOW terminal.

In consideration of safety for the operator and the environment, the output terminal of HIGH-side has been placed in the most distant location from the control area. The free rotation machanisim protects from twisting (or breaking) of the cable. Also, with having the lock function for the LOW terminal on the main unit, the metal plate is no longer attached to the test lead of LOW-side, and it makes to resist damage to the test lead. Because of elimination of these projected components, the TOS5300 can avoid from unexpected accidents such as when the unit travels to other location. And also when the test lead is snagged on something, or unexpected stress is applied on the test lead, the High (High-voltage) test lead is designed to disconnect easily, but the Low (ground) test lead is designed to resist disconnection.

In order to prevent the insertion error, the color coding of the cable are classified to HIGH (red) and LOW (black) , and the plug shape of terminal are also different design.



 Flat surface design of the HIGH terminal with free rotation mechanism, and the LOW terminal with lock function

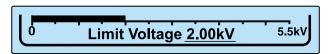


It could be a cause of defect if the cable are incorrectly wired for the HIGH (High-voltage) side and LOW (Low-voltage) side. In order to prevent the insertion error, the protection plate is attached to the Low-voltage (Black) test lead.

Monitoring the output voltage and protection when applying an overvoltage by the operation error

LIMIT VOLTAGE function

Prevents the user from setting a test voltage that exceeds the preset voltage.



Monitoring output voltage function

If the output voltage exceeds the setting voltage of (\pm 350V), it turns off the output and the system switches to PROTECTION mode. In order to handle kilo's of high voltage when the Hipot (Withstand voltage) and insulation tests are conducted, there are number of safety measures are required to take place. Having with these functions improve, the operational safety and the protection for the EUT.

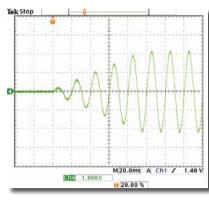
Rise time / Fall time control function

Rise time control function

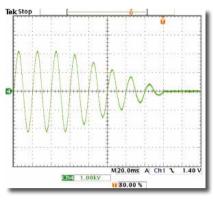
The Rise time control function enables you to increases the test voltage gradually to reach the setting voltage while the AC Hipot (Withstanding voltage) test is conducted. The voltage rise time can be set from 0.1s to 10.0s at a resolution of 0.1s.

Fall time control function

The Fall time control function enables you to decrease the test voltage gradually when the PASS judgment is made at the AC Hipot (Withstanding voltage) test. The voltage fall time is fixed at 0.1 s. (OFF is also selectable).



Rise time control waveform (example)



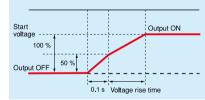
Fall time control waveform (example)

POINT

The rise time control function is to prevent the EUT (test object) from being exposed to stress that exceeds the required amount. The Hipot (Withstanding voltage) test is conducted to verify the safety performance of the EUT and which test voltage for Hipot (Withstanding voltage) test is applied approximately five to ten times greater than the voltage that handles by the EUT. If a high voltage is applied rapidly with no rise time, the transitional large voltage (current) will be occurred, and it may cause a damage to the EUT. If, as a result of the test, the EUT is suffered of the insulation (dielectric) breakdown, the failure of defect can easily be identified without any problem. However, when breakage is occurred partially, it becomes hard to identify the problem.

In other words there are cases in which "at a glance, a Hipot (Withstanding voltage) test appears to have been successfully passed, however, the fact is found that the insulation performance has degraded." In such cases there is a potential risk of danger that the insulation failure will occur after the EUT has been released into the market as a commercial product. The result of testing performed to confirm safety may cause the loss of product's safety. For this reason, safety standards stipulate the procedure of Hipot (Withstanding voltage) test, and the test voltage must be gradually increased to the specified voltage when the test is performed. The rise time control function adopted in the in the TOS5300 Series can set the voltage rise time from 0.1s to 10.0s (at a resolution

of 0.1 s) and also it is capable to set the 50% (fixed) of the applied test voltage. In addition, the fall time control function enables to decrease the test voltage gradually after the completion of a PASS judgement. The voltage fall time is fixed at 0.1s (OFF is also selectable).



▲ Start voltage can be set at 50% of the test voltage.

Examples of Safety Standards (Routine Tests)

IEC60950/J60950

- Information processing equipment

The test voltage applied to the insulation part should be increased gradually from zero to the specified voltage, then hold at the specified voltage for 60 seconds.

IEC60335/J0335 - Home appliances

The initial test voltage should be applied less than half of the specified voltage, then gradually increase to the specified voltage.

IEC60065/J0065 - Audio/video

The initial test voltage should be applied less than half of the specified voltage, then rapidly increase to the specified voltage and hold for 1 minute.

• IEC61010/JIS C 1010

-Measurement equipments

Avoids any detectable transient phenomenon, the test voltage should be increased gradually to the specified voltage within 5 seconds, then hold at the specified voltage for 5 seconds.



Q.What is a Hipot (Withstanding voltage) test?

A. Withstanding test also called a dielectric strength test or Hipot test, a withstanding voltage test is intended to verify whether an electrical product or part has sufficient dielectric strength with respect to the voltage being handled.

Q.What is PASS / FAIL criteria ?

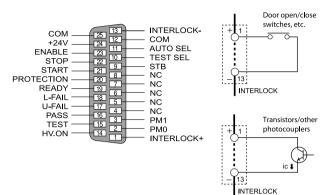
A. It is considered as "Electrical breakdown" when the current exceeds the limit value flowing through the insulated section during a test. If "the Electrical breakdown" does not occur, the insulator is determined to have sufficient insulating strength.

Q.How is the test conducted?

A. Apply the voltage with much higher stress than it would normally be applied to the insulated section for the specified time period. While testing, it evaluates to verify whether any insulation breakdown has occurred on the insulator.

Interlock feature

The product is equipped with an interlock function that operates together with external devices to interrupt output. To ensure the safe operation of tester, the interlock function activates when the SIGNAL I/O connector pins number 1 and 13 are opened, and when they are short-circuited, the interlock function is released.





The picture shown above indicates the caution sign of "HI VOLTAGE DANGER" with a chain surrounding the test site.

Discharge feature (Model TOS5301 / TOS5302)

Equipped with a forced discharge function that forcibly discharge the electricity which has been charged in the EUT after the completion of DC Hipot (Withstanding voltage) test or insulation resistance test.



Q.What is an insulation resistance test?

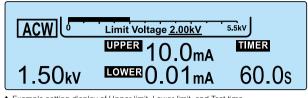
A. An insulation resistance test is to measure the resistance value of insulator and verify that whether the insulator has a sufficient performance. It is similar to the Hipot (Withstanding voltage) test that confirms the function or performance of an insulator, and it should be the required conditions to prevent the accidents from an electrical shock and fire.

Q.What is the procedure of testing?

A. In many cases, after moisture is absorbed (or is not absorbed in some cases), 500 volts or other specified value of DC voltage is applied, and the resistance value is measured from the current flowing.

Upper limits / Lower limits setting function

It automatically detects connector lead breaks and disconnections of wiring by measuring extremely small amounts of current that flows when voltage is applied to the EUT.



▲ Example setting display of Upper limit, Lower limit, and Test time

POINT

Normally, even with a good-quality EUT, a certain degree of leakage current flows. If the current value is set at slightly smaller than the specific range of the EUT, it is useful in detecting breaks of the test lead and faulty connections, which enables tests to be performed with even higher reliability. You can perform testing effectively if you set the lower limit value with LOWER ON during Hipot (Withstanding voltage) test, and the upper limit value with UPPER ON during insulation resistance test.

Calibration due notice and Warning function

To assure the traceability of periodic calibration of the product, this function gives a notice of calibration due managed by the builtin real-time clock. Even if the due data has elapsed, it is possible to avoid the oversight of operator with limiting the operation with a display of warning message.

3.Date and Time		1 Menu
Set Date and Time	20 <u>00</u> / 1/30 2:33:48	2 Up
Factory Calibration Calibration Due Control	2000/1/1 3 months	3 Down
Calibration Protection	OFF	Apply 4

▲ Example setting display of Calibration due

Q.What is the difference between an insulation resistance test and a Hipot test?

A. The Hipot (Withstanding voltage) test detects a faulty insulation whether insulation breakdown occurs. In contrast, the insulation resistance test detects faulty insulation by measuring the resistance value.

Easy setting of test conditions with

panel memory feature!

_	MEMORY
	1
	2
	3
	RECALL

To set the test conditions such as test voltage, judgment value and test duration, simply press a key and turn the knob on the front panel. The test conditions can be saved in the panel memory (3 sets).

Panel memory setting key

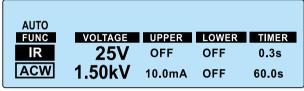
(Model TOS5302)

AUTO TEST feature for consecutive testing

The TOS5302 can perform an AC Hipot (Withstanding voltage) test and an insulation resistance test consecutively.

Either of the following can be conducted :

Insulation resistance test \rightarrow AC Hipot (Withstanding voltage) test, or AC Hipot (Withstanding voltage) test \rightarrow Insulation resistance test.



 \blacktriangle Insulation resistance test \rightarrow AC withstand-voltage test

AUTO				
FUNC	VOLTAGE	UPPER	LOWER	TIMER
ACW	1.50kV	0.02mA	OFF	60.0s
IR	25V	OFF	OFF	0.3s
	234	U .1	0.1	0100

▲ AC withstand-voltage test → Insulation resistance test

REMOTE connector & USB interface

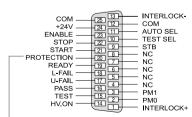


Equipped with the REMOTE connector and USB interface on the front panel are exclusive use for the options. Easy connection with the PC.

SIGNAL I/O Connector

The rear panel is equipped with SIGNAL I/O that provides functions such as start and stop operation and signal output.

PIN No	Signal name	I/O	TOS5300	TOS53	01			TOS5302
1	INTERLOCK+	I	When + terminal and - and the system shifts t Open: Terminal-to-tern Short: Terminal-to-tern	o the Pı ninal re	otect sistar	ion s ice is	tatus. 1.2 kΩ	2 or more
2	PM0	Ι	Panel memory selection		PM0	PM1	Callec	Panel Memory Number
3	PM1	Т	When input strobe signa up latch selection signal		н	н	Memo	
			*Memory selection prior from TEST SEL, AUTO SE	ritizes	L H L	H L L	Memo Memo Enable	
4	NC	—			_	-		
5	NC	—			_	-		
6	NC	_			_	-		
7	NC	—			_	-		
8	NC	_			_	-		
9	STB	Т	Panel mer	nory's s	trobe	sign	al inpi	ut terminal
10	TEST SEL	I	NA	ACW/DCW selection signal L: ACW H: DCW				Single/Independent test's selection signal/ AUTO Test's sequence selection signal Selection of single test with AUTO SEL L: ACW; H: DCW Selection of AUTO test with AUTO SEL L: ACW \rightarrow IR H: IR \rightarrow ACW
11	AUTO SEL	I	NA NA				Selection of AUTO test/single test L: Single test H: AUTO test	
12	СОМ	_	Circuit's common term	inal				
13	INTERLOCK-	I	When + terminal and - and the system shifts t Open: Terminal-to-terr Short: Terminal-to-terr	o the Pı ninal re	otect sistar	ion s ice is	tatus. 1.2 kΩ	2 or more
14	HV.ON	0	ON during test and wh between output termi		age re	emair	15	ON during test, while voltage remains between output terminals, and during automatic test (AUTO TEST)
15	TEST	0	ON during test (except	when	/oltag	je is r	ising	or falling)
16	PASS	0	ON for at least 0.2 sec. time) Continuously ON					etermined (PASS HOLD is set for HOLD
17	U-FAIL	0	Continuously ON when and UPPER FAIL is dete			accep	table	maximum is detected,
18	L-FAIL	0	Continuously ON when detected, and LOWER					le minimum is
19	READY	0	ON during standby (RE	ADY sta	atus)			
20	PROTECTION	0	ON while protection fu	inction	is act	ivate	d (PRC	DTECTION ON)
21	START	Т	Start signal input term	inal				
22	STOP	Ι	Stop signal input term	inal				
23	ENABLE	I	Start signal's ENABLE s Shifts to the Protectior					LE signal changes
24	+24V	_	+24V internal power supp	ly outpu	t term	inal: N	laximu	um output current 100 mA
25	сом		Circuit's common term	linal				





Rear panel

Specifications -Withstanding voltage tester

Unless specified otherwise, the specifications are for the following settings and conditions. • The warm-up time is 30 minutes. • TYP:These are typical values. These values do not guarantee the performance of the product. • rdng: Indicates the readout value.

- set: Indicates a setting.
 f.s: Indicates full scale.

				TOS5300	тс	\$5301	TO\$5302		
	Output rang	e			0.05 k\	′ to 5.00 kV			
		Accuracy			±(2 % of set + 20 V) w	hen no load is connected			
		Setting range	0.00 kV to 5.50 kV						
		Resolution			10	V steps			
	Max. rated o	utput *1		500 VA (5 kV/100 mA)					
	Max. rated v	oltage	5 kV						
	Max. rated c	urrent			100 mA (when the outpu	t voltage is 0.5 kV or great	er)		
AC output	Transformer	rating			5	00 VA			
section	Output voltage waveform *2			Sine					
		Distortion		Sine If the output voltage is 0.5 kV or more: 3 % or less (when no load or a pure resistive load is connected).					
	Frequency					or 60 Hz	,		
	,	Accuracy				uring voltage rise time)			
	Voltage regu			10 %		om maximum rated load to	no load)		
	Input voltag					ted; power supply voltage:			
	Short-circuit					utput voltage is 1.0 kV or g			
				200					
	Output met					switching			
	Output rang	e				(to 6.00 kV			
		Accuracy				f set + 20 V) ad is connected			
		Setting range				' to 6.20 kV			
		Resolution				V STEP			
	Max. rated o					kV / 10 mA)			
		· · · · · · · · · · · · · · · · · · ·				6 kV			
DC output	Max. rated voltage Max. rated current								
section	Max. rated c			_	I	0 mA	—		
	Ripple (TYP)	5 kV when no load is connected			50) Vp-p			
		Max. rated load			10	0 Vp-p			
		1-41			3% or less (When c	hanging from maximum			
	voltage regu	Voltage regulation			rated load	d to no load))			
	Short-circuit	current (TYP)			40 mA (when ger	neration 6 kV output)			
	Discharge fe	ature				after test completion sistance: 125 k Ω)			
Start Voltage	2			The voltage at the		tage tests can be set to 50	% of the test voltage.		
Limit Voltage				-		AC: 0.00 kV to 5.50 kV, DC			
		turo				+ 350 V or is lower than the			
Output volta	ige monitor fea	iture		0	utput is turned off, and p	otective features are active	ated.		
		Scale			6 kV	AC/DC f.s			
	Analog	Accuracy			±	5 % f.s			
		Indication			Average value	response/rms scale			
		Measurement			0.000 1311				
Voltmeter		range			0.000 kV to	6.500 kV AC/DC			
		Display				kV			
	Digital	Accuracy		V	< 500 V: ±(1.5 % of rdng +	20 V); V ≥ 500 V: ±1.5 % of	rdng		
		Response			True rms (resp	onse time: 50 ms)			
		Hold feature		After a test is finished	the measured voltage is	retained until the PASS or I	FAIL judgment is cleared.		
		Measurement		00 m 4 to 110 m 4	AC: 0.00 r	nA to 110 mA			
		range	AC: 0	.00 mA to 110 mA	DC: 0.00	mA to 11 mA	AC: 0.00 mA to 110 mA		
			i = measured cu	rrent					
		Display		i < 1 mA	1 mA ≤ i < 10 mA	10 mA ≤ i < 100 mA	100 mA ≤ i		
Ammeter	Digital	Cispidy		μΑ	mA	mA	mA		
				1					
		Accuracy *3		1.00 r		< 1.00 mA: ±(1.5 % of rdng	+ 30 μΑ)		
		Response			True rms (resp	onse time: 50 ms)			
		Hold feature		After a test is finis	ned, the measured voltag	e is retained until the PASS	judgment is cleared.		

			Judgment	Display	Buzzer	SIGNAL I/O			
			UPPER FAIL	If a current that is greater t detected, the output is turn turned off, and an UPPER FAIL rise time (Rise Time) of DC with	ment method than or equal to the upper limit is led off, and an UPPER the output is judgment occurs. During the voltage istanding voltage tests, an UPPER FAIL s a problem with the voltage rise ratio.	FAIL LED lights	ON	Generates a U-FAIL signal	
	udgment meti		LOWER FAIL	the output is turned off, an This judgment is not perfor	r equal to the lower limit is detected, Id a LOWER FAIL judgment occurs. med during voltage rise time (Rise the voltage fall time (Fall Time) of AC	FAIL LED lights UNDER is displayed on the screen	ON	Generates a L-FAIL signal	
Judgment			PASS	If the specified time elapses turned off, and a PASS judgm	without any problems, the output is nent occurs.	PASS LED lights	ON	Generates a PASS signal	
			The UPPER FAIL The FAIL and PA For PASS judgm	and LOWER FAIL signals are g ASS buzzer volume levels can l	the buzzer sounds for is fixed to 0.2 se	5300 Series receives a			
U	Upper limit set	tting	AC: 0.	AC: 0.01 mA to 110 mA DC: 0.01 mA to 110 mA				AC: 0.01 mA to 110 mA	
Lo	ower limit set	ting	AC: 0.01	AC: 0.01 mA to 110 mA / OFF DC: 0.01 mA to 110 mA / OFF					
Ju	udgment accu	uracy *3		1.00 m/	A ≤ i: ±(1.5 % of set), i < 1.00 mA: ±(1.5	5 % of set + 30 µA)			
Cr	Current detect	ion method		Calculates the curre	nt's true rms value and compares this	value with the refere	nce valu	e	
C	Calibration			Calibrate	ed with the rms of a sine wave using a	a pure resistive load			
Ve	oltage rise tin				0.1 s to 10.0 s				
		Resolution			0.1 s				
Time 🗕	/oltage fall tim	ie		0.1 s	/ OFF (only enabled when a PASS jud				
1 T.	fest time				0.1 s to 999 s, can be turned off (TII				
16	1	Resolution	0.1 s to 99.9 s: 0.1 s. 100 s to 999 s: 1 s. ±(100 ppm + 20 ms) excluding Fall Time						

*1. Regarding the output time limits:

Taking size, weight, and cost into consideration, the heat dissipation capability of the voltage generator that is used for withstanding voltage tests has been designed to be one half that of the rated output. Use the TOS5300 Series within the following limits. If you use the product in a manner that exceeds these limits, the output section may overheat, and the internal protection circuits may be activated. If this happens, stop testing, and wait until the TOS5300 Series returns to its normal temperature.

Ambient temperature	Upper	limit	Pause time	Output time		
	AC	$50 \text{ mA} < i \le 110 \text{ mA}$	50 mA < i \leq 110 mA Greater than or equal to the output time			
t < 40 °C	AC	i ≤ 50 mA	Not necessary	Continuous output possible		
l ≦ 40 C	DC	5 mA < i ≤ 11 mA	Greater than or equal to the output time	1 min. max.		
	DC	i ≤ 5 mA	Greater than or equal to the wait time (WAIT TIME)	Continuous output possible		
			(Output time - veltage vice ti	na i tast tima i valta sa fall tima)		

(Output time = voltage rise time + test time + voltage fall time)

*2. Regarding the test voltage waveform:

Waveform distortions may occur if an DUT whose capacitance is dependent on voltage (for example, an DUT that consists of ceramic capacitors) is connected as the load. However, if the test voltage is 1.5 kV, the effect of a capacitance of 1000 pF or less can be ignored. Because the product's high-voltage power supply uses the PWM switching method, if the test voltage is 500 V or less, the switching and spike noise proportions are large. The lower the test voltage, the greater the waveform is distorted.

*3. Regarding ammeter and judgment accuracy:

During AC withstanding voltage tests, current also flows in the stray capacitance of items such as the measurement leads and jigs. This current that flows in the stray capacitances is added to the current that flows in the DUT, and the sum of these currents is measured. Especially if you want to perform judgments with high sensitivity and accuracy, it is necessary to consider methods to limit the current that flows in these stray capacitances, such as by adding upper and lower limits.

Output voltage	1 kV	2 kV	3 kV	4 kV	5 kV
When using 350 mm long test leads that are suspended in air (TYP)	2 μΑ	4 μΑ	6 μΑ	8 μΑ	10 µA
When using the accessory, high test lead TL31-TOS (TYP)	16 µA	32 µA	48 µA	64 µA	80 µA

							TOSS	302				
	Output voltag	ge				25 V, 50 V, 100	V, 125 V, 250 V	, 500 V, 1000 V	DC (nega	tive)		
		Accuracy					-0 %,					
	Max. rated loa						1 W (-1000 V					
	Max. rated cu	rrent					1 n	۱A				
	Ripple	1000 V when no load is connected					2 Vp-p	or less				
Output section	hipple	Max. rated load	10 Vp-p or less									
	Voltage regula	ation			1%	or less (when c	hanging from	maximum rate	ed load to	no load)		
	Short-circuit	current					12 mA	or less				
	Discharge fea					scharge after te						
	Limit voltage					• • •				0 V, 500 V, 1000 V		
	Output voltag	ge monitor feature	If output volt	age exceeds "	10 % of set + 10	0 V" or is lower			utput is tu	rned off, and protec	tive feature	s are activated.
	A	Scale					6 kV AC					
	Analog	Accuracy Indication				۸.,	± 5 %					
			Average value response/rms scale									
Volt- meter		Measurement range					0 V to -	1200 V				
	Digital	Display		Measured	d voltage	V < 1	00 V		V < 1000	/ 100	0 V ≤ V	
		2.5pidy	L	Disp	olay		V		V			
		Accuracy					± (1 % of r	dng + 1 V)				
						0.03 MΩ ≤	≤ R ≤ 25 MΩ / ±	(2 % of rdng -	+ 2 digits)			
		25 V				25 N	ΛΩ < R ≤ 125 Ν ΛΩ < R ≤ 250 Ν	$\Lambda\Omega/\pm 5\%$ of re	dng			
						0.05 MΩ ≤	≤ R ≤ 50 MΩ / ±	:(2 % of rdng -	+ 2 digits)			
		50 V					$\Lambda\Omega < R \le 250 \text{ N}$					
							<i>I</i> Ω < R ≤ 500 N					
		100 V					$M\Omega \le R \le 100$ $M\Omega < R \le 500$ I					
	Measurement	100 v					$M\Omega < R \le 1 G\Omega$					
	range /		$0.125 \text{ M}\Omega \le R \le 125 \text{ M}\Omega / \pm 2\% \text{ of rdng}$									
	measurement accuracy *4 *5	125 V	$125 \text{ M}\Omega < \text{R} \le 625 \text{ M}\Omega / \pm 2\%$ of rding									
Resistance meter			625 M Ω < R \leq 1.25 G Ω / \pm 10 % of rdng									
meter				$0.250 \text{ M}\Omega \leq R \leq 250 \text{ M}\Omega / \pm 2\% \text{ of rdng}$								
		250 V	$250 \text{ M}\Omega < \text{R} \le 1.25 \text{ G}\Omega / \pm 5\% \text{ of rdng}$ 1.25 GO < B < 2.5 GO / +10% of rdng									
			$1.25 \text{ G}\Omega < \text{R} \le 2.5 \text{ G}\Omega / \pm 10 \text{ \% of rdng}$									
		500 V		0.50 M $\Omega \le R \le 500$ M $\Omega / \pm 2$ % of rdng 500 M $\Omega < R \le 2.5$ G $\Omega / \pm 5$ % of rdng								
		500 1		$S00 M\Omega < R \le 2.5 G\Omega / \pm 5 \%$ of rdng 2.5 $G\Omega < R \le 5 G\Omega / \pm 10 \%$ of rdng								
		40001/		$1 \text{ M}\Omega \leq \text{R} < 1 \text{ G}\Omega / \pm 2\% \text{ of rdng}$								
		1000 V	$1 \text{ G}\Omega \leq \text{R} \leq 5 \text{ G}\Omega / \pm 5 \% \text{ of rdng}$									
			25 kΩ ≤ R <	- 1.00 MO	1.00 MO < 1	R < 10.0 MΩ	10.0 MΩ ≤ I	R < 100 MO	100.0 M	$M\Omega \leq R < 1.00 G\Omega$	1.00.60	≤ R ≤ 9.99 GΩ
	Display *5					MΩ		ΜΩ				GΩ
					1							
Hold feat	ure etection respo	nco chood		Afte	er a test is finisr		hed between t			SS judgment is clear	red.	
Current u		iise speed				Can be switch			st, iviiu, si	000		
			Judgment			Judgment				Display	Buzzer	SIGNAL I/O
			UPPER FAIL							FAIL LED lights; OVER is displayed	ON	Generates
					ned during volt			.curs. mis juu	gineneis	on the screen		a U-FAIL signal
						•		imit is detect	ed or if a	FAIL LED lights;		Generates
			LOWER FAIL		curs during the		ime (Rise Time	e), the output	is turned		d ON	a L-FAIL signal
	Judgment me			off, and a LO	OWER FAIL judg	gment occurs.				on the screen		
	judgment ope	eration	PASS		ied time elapse judgment occu		problems, the	output is turn	ed off,	PASS LED lights	ON	Generates a PASS signal
Judgment			• If PASS HOLD is	enabled, the	PASS signal is	generated con	tinuously unti	l the TOS5300	Series red	eives a STOP signal		
feature							ntinuously un	til the TOS530	0 Series re	eceives a STOP signa	al.	
			The FAIL and P				unds for is five	ad to 0.2 secor	nde			
			 For PASS judgments, the length of time that the buzzer sounds for is fixed to 0.2 seconds. Even if PASS HOLD is enabled, the buzzer turns off after 0.2 seconds. 									
	Upper limit se	etting range	0.03 MΩ to 5.00	GΩ								
	Lower limit se	etting range	0.03 MΩ to 5.00	GΩ								
			Measurement a									
	Judgment acc		Humidity: 20 %r For judgments c						leads or o	other problems.		
	(the same for	UPPER and LOWER)	If the current de						is necessa	ary.		
			If the current de									
	Voltage rise ti	me	10 ms(TYP)									
Time	Test Time	[0.1 s to 999 s, ca)						
	<u> </u>	Resolution	0.1 s to 99.9 s: 0		999 s:1 s.							
1	Accuracy		± (100 ppm + 2	0 ms)								

*4. Humidity: 20 %rh to 70 %rh (no condensation). No bends in the test leads. *5. R = measured insulation resistance

Specifications - Other features / Interfaces

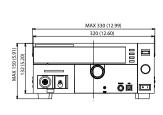
		TOS5300	TOS5301	TOS5302			
Double action feature		Tests can only be started by pressing and releasing STOP and then pressing START within 0.5 seconds of releasing the STOP switch.					
Length of time to maintain	n a PASS judgment result	You can set the length of time to maintain a PASS judgment: 50 ms, 100 ms, 200 ms, 1 s, 2 s,5 s, or HOLD.					
Momentary feature		Tests ar	e only executed while the START switch is hel	d down.			
Fail mode feature		This feature enables you to prevent rem	otely transmitted stop signals from clearing F	AIL judgments and PROTECTION modes.			
Timer feature		This fe	ature finishes tests when the specified time e	lapses.			
Output voltage monitor fe	ature		ge exceeds "setting + 350 V" or is lower than " tches to PROTECTION mode, output is turned				
Memory		Up to t	hree sets of test conditions can be saved to m	iemory.			
Key lock		Lo	cks panel key operations (settings and change	es).			
Protective features			the TOS5300 Series switches to the PROTECTI tops testing. A message is displayed on the so				
Interlock Protect	tion		An interlock signal has been detected.				
Power Supply Pr	otection		An error was detected in the power supply.				
Volt Error Protec	tion	While monitoring the output voltage, a voltage outside of the rated limits was detected. AC or DC withstanding voltage tests: ±350 V Insulation resistance test: ±(10 % of set + 10 V)					
Over Load Prote	ction	During a withstanding voltage test, a value that is greater than or equal to the output limit power was specified. AC withstanding voltage test: 550 VA. DC withstanding voltage test: 55 VA.					
Over Heat Prote	ction	The internal temperature of the TOS5300 Series became too high.					
Over Rating Prot	tection	During a withstanding voltage test, the	e output current was generated for a length o	f time that exceeds the regulated time.			
Calibration Prote	ection		The specified calibration period has elapsed.				
Remote Protecti	on	A connection to or di	sconnection from the front-panel REMOTE co	nnector was detected.			
SIGNAL I/O Prote	ection	The rear-pa	anel SIGNAL I/O connector's ENABLE signal ha	is changed.			
USB Protection		The USB connector has been discon	nected while the TOS5300 Series was being c	ontrolled through the USB interface.			
System clock		Set in the fo	bllowing format: year/month/day hour/minut	es/seconds.			
Calibration date			Set when the TOS5300 Series is calibrated.				
Calibration perio	d setting	Sets	the period before the next calibration is nece	ssary.			
Notification of w period elapses	hen the calibration	Sets the operation that is performed when the specified calibration period elapses. When the TOS5300 Series turns on, it can display a notification or switch to the protection mode and disable testing.					
US	В		USB Specification 2.0				
Interfaces RE	MOTE	Front-panel 9-pin MINI DIN connector. By connect	ing an optional device to this connector, you can c	ontrol the starting and stopping of tests remote			
SIC	SNAL I/O	Rear-panel D-sub 25-pin connector					

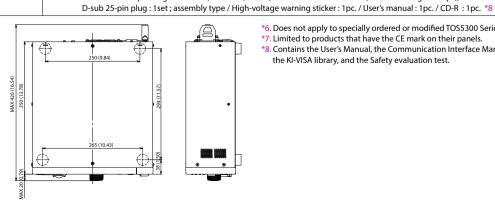
Specifications - General

			TOS5300	TOS5301	TOS5302			
Display				VFD: 256 × 64 dots + 4 status indicators				
Backup battery life 3 years (at 25 °C or 77 °F)								
	Installation I	ocation		Indoors, at a height of up to 2000 m				
	Spec guarantee	d Temperature		5 °C to 35 °C (41 °F to 95 °F)				
L	range	Humidity		20 %rh to 80 %rh (no condensation)				
Environ- ment	Operating	Temperature		0 °C to 40 °C (32 °F to 104 °F)				
ment	range	Humidity		20 %rh to 80 %rh (no condensation)				
	Ch	Temperature		-20 °C to 70 °C (-4 °F to 158 °F)				
	Storage rang	e Humidity		90 %rh or less (no condensation)				
_	Nominal vol (allowable v	tage range oltage range)		100 VAC to 240 VAC (90 VAC to 250 VAC)				
Power supply	Power V	/hen no load is connected (READY)						
suppry	consumptio V	Vhen rated load isconnected	800 VA max.					
	Allowable fr	equency range	47 Hz to 63 Hz					
Insulation	resistance (betw	veen AC LINE and the chassis)		30 MΩ or more (500 VDC)				
Withstand	ling voltage (be	tween AC LINE and the chassis)		1390 VAC, 2 seconds, 20 mA or less				
Earth cor	ntinuity			25 AAC, 0.1 Ω or less				
Safety *6			Complies with the requirements of the followin	g directive and standard. Low Voltage Directive 2	006/95/EC, EN 61010-1 Class I Pollution degree 2			
Electromagnetic compatibility (EMC) *6 *7			Applicable under the following cor	ng directive and standard. EMC Directive 2004/1 nditions The maximum length of all cabling an st lead TL31-TOS is being used. Electrical disch	d wiring connected to the TOS5300			
Dimensio	ons			See "Outline drawing."				
Weight			Approx. 14 kg (30.9 lb.)	Approx. 15 kg (33.1 lb.)	Approx. 14 kg (30.9 lb.)			
Accessor	ies			l (TL31-TOS) : 1set (1 red wire and 1 black wire y type / High-voltage warning sticker : 1pc. / U	5 1			

Outline drawing

Unit: mm (inch)





*6. Does not apply to specially ordered or modified TOS5300 Series testers.
*7. Limited to products that have the CE mark on their panels.
*8. Contains the User's Manual, the Communication Interface Manual, the KI-VISA library, and the Safety evaluation test.

Ordering information

Product Name	Model	Remarks
AC Hipot (Withstanding voltage) & Insulation Resistance tester (ACW/IR)	TO\$5302	Hipot (Withstanding voltage) test: AC 5 kV/100 mA Insulation Resistance test: 25V - 1000V
AC/DC Hipot (Withstanding voltage) tester (ACW/DCW)	TOS5301	Hipot (Withstanding voltage) test: AC 5 kV/100 mA, DC 6 kV/50 W
AC Hipot (Withstanding voltage) tester (ACW)	TOS5300	Hipot (Withstanding voltage) test: AC 5 kV/100 mA

Options



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